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Docket JP920010326US1

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Appl. No.: 10/736,343

Filed: December 15, 2003

**In the United States Patent and Trademark Office**In re the application of:  
Rajendra K. Bera

Filed: 12/15/2003

For: Run-Time Parallelization  
of Loops in Computer  
Programs

Appl. No.: 10/736,343

Applicant's Docket:  
JP920010326US1

Group Art Unit: 2183

Examiner: Ryan Paul Fiegler

**CERTIFICATE OF FACSIMILE TRANSMISSION**I hereby certify that this correspondence is being transmitted via facsimile  
to the United States Patent Office on the date indicated by my signature below.*Anthony V.S. England*  
Anthony V.S. England*9-29-2006*  
Date**AMENDMENT AFTER REPLY A**

The following amendment is responsive to discussions with Examiner Fiegler after the  
filing of Applicant's Reply A.